

FORM PTO-892 (REV. 2-92)		US DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 09/389,567	GROUP ART UNIT 2183	ATTACHMENT TO PAPER NUMBER 6
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D	5,389,838	02-14-1995	Orengo	326	93	
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R	Ohta et al., <i>New FPGA Architecture for Bit-Serial Pipeline Datapath</i> , Proceedings of the IEEE Symposium on FPGAs for Custom Computing Machines, pp. 58-67, April 15-17, 1998.					
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EXAMINER Richard Ellis		DATE April 29, 2003				
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)						